

**Amendments to the Claims:**

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) A display device, comprising:

a display ~~drive~~ section having a plurality of scanning lines and a plurality of data lines formed in a grating form corresponding to dots as minimum units of display and active elements provided corresponding to ~~intersections, the display drive performs display control using a liquid crystal by driving said scanning lines and said data lines, said display drive further being integrated on a semiconductor or an insulating substrate and integrally formed therewith;~~ intersections;

a scanning line driver that selects and drives said scanning lines, ~~the scanning line driver being allocated corresponding to a length in a column direction of said display drive;~~

a memory having a plurality of memory cells that are capable of storing an image signal for performing display control of dots in at least one row of said display drive, ~~the memory being allocated corresponding to the length in a row direction of said display drive, said memory cells being integrated on said semiconductor or said insulating substrate and integrally formed therewith;~~ section, the plurality of memory cells being arranged in a matrix having a plurality of rows and a plurality of columns;

a column decoder ~~allocated corresponding to the length in the row direction of said display drive, the column decoder~~ that selects said memory cells for storing an input image signal;

a column selection switch section ~~allocated corresponding to the length in the row direction of said display drive,~~ to switch on the basis of a selection by said column

decoder and the image signal and storing the image signal to said memory cells selected by said column decoder; and

~~a data line driver allocated corresponding to the length in the row direction of said display drive, the data line driver that~~ drives said data lines on the basis of the image signal stored in said ~~memory, the data line driver further being integrated on said semiconductor or said insulating substrate and integrally formed therewith.~~ memory,

the memory being disposed between the display section and the column selection switch section, and

the display section, the memory, and the column selection switch section being formed on one substrate.

2.-8. (Canceled)

9. (Currently Amended) A display device as claimed in claim 1, wherein the number of said memory cells, which are allocated corresponding to the length in the row direction of said display ~~drive section~~ and capable of storing the image signal for display control of the dots on one row of said display ~~drive section~~, is structured redundantly.

10. (Currently Amended) A display device as claimed in claim 1, wherein said memory connects said memory cells in the number capable of storing an image signal for display control of the one-row dots to each of word lines in the number equal to the number of said scanning lines and is structured with a memory array corresponding to dot arrangement of said display ~~drive section~~, and

a word line driver that selects and drives said word lines are further integrated on and integrally formed with said substrate.

11. (Previously Presented) A display device as claimed in claim 10, wherein, on the basis of an address signal representative of a display position and a storage position, said

scanning line driver selects said scanning lines and said word line driver selects said word lines.

12. (Previously Presented) A display device as claimed in claim 11, wherein the same address signal is inputted to said scanning line driver and said word line driver.

13. (Previously Presented) A display device as claimed in claim 11, wherein independent address signals are inputted to said scanning line driver and said word line driver.

14. (Previously Presented) A display device as claimed in claim 11, wherein said scanning line driver operates to select and drive said scanning lines on the basis of the address signal only when a scanning line driver control signal is inputted, and said word line driver operates to select and drive said word lines on the basis of the address signal only when a word line driver control signal is inputted.

15. (Previously Presented) A display device as claimed in claim 11, said column decoder section selecting the memory cell to store an inputted image signal on the basis of the address signal.

16. (Previously Presented) A display device as claimed in claim 15, wherein one pixel comprises three dots provided for developing and displaying red, blue and green as light source colors, the image signal is inputted on the basis of a unit of one-pixel, and said column decoder selects the memory cell in an amount of one pixel.

17. (Previously Presented) A display device as claimed in claim 15, wherein one pixel comprises three dots provided for developing and displaying red, blue and green as light source colors, the image signal is inputted on the basis of a unit of a plurality of pixels, and said column decoder selects the memory cell in an amount of a plurality of pixels.

18. (Currently Amended) A display device as claimed in claim 1, wherein an input interconnection for the image signal to be stored in said memory cell and said column

selection switch section are formed on a side opposite to said display ~~drive~~ section sandwiching said memory therebetween.

19. (Currently Amended) A display device as claimed in claim 1, wherein said memory is allocated with the memory cell corresponding to the length in the row direction of said display ~~drive~~ section and formed in a multi-stage structure.

20. (Currently Amended) A display device as claimed in claim 10, said word lines being provided in the number of integer times the number of the scanning lines, and said memory being structured by a memory array connecting, by grouping, the memory cells in the number capable of storing the image signal for display control of the one-row dots of said display ~~drive~~ section to the word lines in the number of the integer times.

21. (Currently Amended) A display device as claimed in claim 1, wherein said memory is structured by a memory array having the memory cells that are in the number capable of storing the image signal for display control of a plurality of rows of the dots of said display ~~drive~~ section and allocated corresponding to the length in the row direction of said display ~~drive~~ section.

22. (Currently Amended) A display device as claimed in claim 1, wherein said memory is structured by a memory array having the memory cells that are in the number capable of storing the image signal for display control of a plurality of rows of the dots of said display ~~drive~~ section and allocated to have a length in the row direction equal to or smaller than the length in the row direction of said display ~~drive~~ section.

23. (Previously Presented) A display device as claimed in claim 22, further comprising:

a timing controller that controls a timing of transmitting the address signal,

and

a memory controller that controls the transmitting of the image signal, the memory controller being integrated on a semiconductor or an insulating substrate and integrally formed therewith.

24. (Currently Amended) A display device as claimed in claim 1, wherein a D/A converter is provided between said display ~~drive~~section and said memory cell that converts the image signal comprising a digital signal stored in the memory cell into an analog signal, followed by supplying to said display ~~drive~~section.

25. (Currently Amended) A display device as claimed in claim 1, wherein said display ~~drive~~section and said memory are directly coupled to supply the image signal comprising a digital signal stored in said memory to said display ~~drive~~section.

26. (Currently Amended) A display device as claimed in claim 25, wherein said display ~~drive~~section performs digital drive through area tonal level, time-division tonal level or a combination thereof.

27. (New) A display device, comprising:

a display section having a plurality of scanning lines, a plurality of data lines and a plurality of dots corresponding to the plurality of scanning lines and the plurality of data lines, each of the plurality of dots including a display element and an active element;

a scanning line driver that drives the plurality of scanning lines;

a data line driver that drives the plurality of data lines;

a memory cell section having a plurality of memory cells, the plurality of memory cells being arranged in a matrix having a plurality of rows and a plurality of columns; and

a selection switch section that controls transmission of an image signal to the plurality of memory cells, each of the plurality of memory cells being capable of storing an image signal supplied through the selection switch section, the memory cell section being

disposed between the display section and the selection switch, and the display section, the memory cell section, and the selection switch section being formed on one substrate.

28. (New) The display device according to claim 27, the memory cell section storing image signals for one screen.

29. (New) The display device according to claim 27, further comprising a plurality of word lines that extend along a row direction that intersects a column direction along which the plurality of data lines extend.

30. (New) The display device according to claim 27, the number of the plurality of rows being equal to the number of the plurality of scanning lines.

31. (New) The display device according to claim 29, further comprising a word line driver that drives the plurality of word lines, a plurality of memory cells that are disposed in the row direction and that are connected to a word line selected by the word line driver among the plurality of memory cells receiving image signals.

32. (New) The display device according to claim 27, further comprising a DAC receiving digital data based on the image signal, the DAC converting the digital data into analog data, the DAC being disposed between the memory cell section and the display section, the DAC being disposed on the one substrate together with the display section, the memory cell section, and the selection switch section.

33. (New) The display device according to claim 27, each of the plurality of memory cells of the memory cell section being configured by any one of a dynamic memory and a static memory.

34. (New) The display device according to claim 27, each of the memory cell of the memory cell section rewriting data based on the image signal without refreshing.

35. (New) The display device according to claim 27, the DAC converting data for a dot of the plurality of dots that is not to be rewritten as it is, the data being stored in a memory cell of the plurality of memory cells.

36. (New) The display device according to claim 27, the memory cell section functioning as a frame memory.

37. (New) A display device, comprising:

a display section having a plurality of scanning lines, a plurality of data lines and a plurality of dots corresponding to the plurality of scanning lines and the plurality of data lines, each of the plurality of dots including a display element and an active element;

a scanning line driver that drives the plurality of scanning lines;

a data line driver that drives the plurality of data lines;

C/ Cmt  
a memory cell section having a plurality of memory cells, the plurality of memory cells being arranged in a matrix having a plurality of rows and a plurality of columns, a length of the matrix in a row direction that intersects a column direction along which the plurality of data lines extend being shorter than a length of the display section in the row direction; and

a selection switch section that controls transmission of an image signal to the plurality of memory cells, each of the plurality of memory cells being capable of storing an image signal supplied through the selection switch section, the memory cell section being disposed between the display section and the selection switch, and the display section, the memory cell section, and the selection switch section being formed on one substrate.

38. (New) A display device, comprising:

a display section having a plurality of scanning lines, a plurality of data lines and a plurality of dots corresponding to the plurality of scanning lines and the plurality of data lines, each of the plurality of dots including a display element and an active element;

a scanning line driver that drives the plurality of scanning lines;

a data line driver that drives the plurality of data lines;

a memory cell section having a plurality of memory cells, a length of the memory cell section in a row direction that intersects a column direction along which the plurality of data lines extend being shorter than a length of the display section in the row direction; and

a selection switch section that controls transmission of an image signal to the plurality of memory cells, each of the plurality of memory cells being capable of storing an image signal supplied through the selection switch section, and the display section, the memory cell section, and the selection switch section being formed on one substrate.

39. (New) The display device according to claim 38, the memory cell section storing image signals for one screen.

40. (New) The display device according to claim 38, further comprising a plurality of word lines that extend along a row direction that intersects a column direction along which the plurality of data lines extend.

41. (New) The display device according to claim 38, the number of the plurality of rows being equal to the number of the plurality of scanning lines.

42. (New) The display device according to claim 38, each of the plurality of memory cells of the memory cell section being configured by any one of a dynamic memory and a static memory.

43. (New) The display device according to claim 38, each of the memory cell of the memory cell section rewriting data based on the image signal only when the data changes.

44. (New) The display device according to claim 38, further comprising a DAC receiving digital data based on the image signal, the DAC converting the digital data into



*could* analog data, the DAC being disposed on the one substrate together with the display section,  
the memory cell section, and the selection switch section.

---